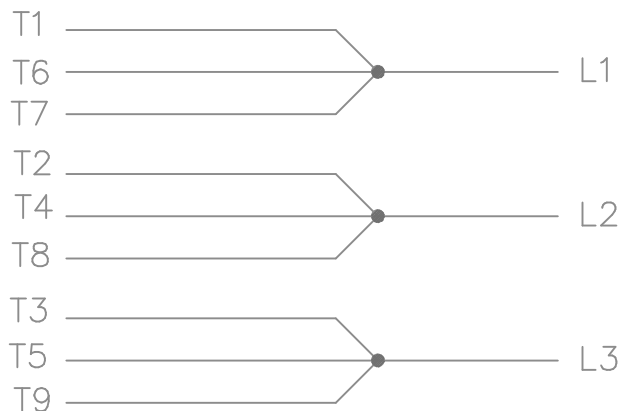
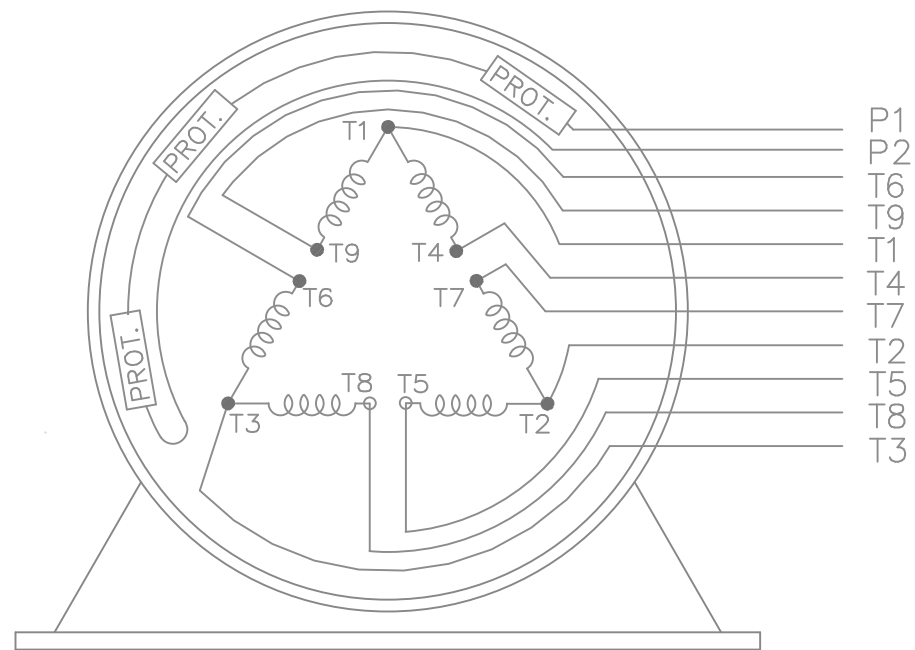
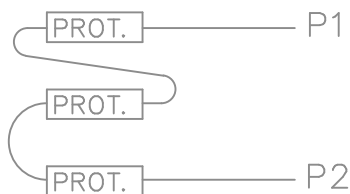
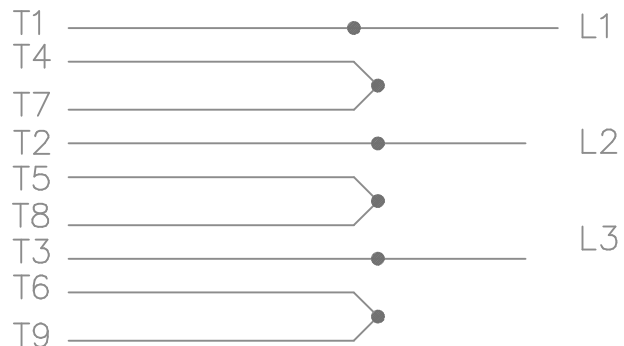


LOW VOLTAGE

EE7308AD




HIGH VOLTAGE



VIEW OF TERMINAL END

WHEN MORE THAN ONE PROT. IS USED; PROT. ARE CONNECTED IN SERIES

				TOLERANCES UNLESS SPECIFIED			DRAWN MJD 12-19-1997			
				DEC.	INCHES		CHK	ML	01-07-1998	
				.X	±.1		APPD	GK	01-07-1998	
				.XX	±.02		SCALE			
				.XXX	±.005		REF			
8	HIGH VOLT. SCHEMATIC WAS L1, L3 & L3	CN 32724	DRS 06-12-2004	ML	.XXX	±.005	TITLE CONNECTION DIAGRAM			
7	REDRAWN ON CADD	MJD 01-07-1998			.XXXX	±.0005	3Ø - WITH PROTECTORS			
NO.	REVISION	BY & DATE	CHK	ANG	±7'30"	FINISH	MAT'L.			
							FMF			
							PREV			
THIS DRAWING IN DESIGN AND DETAIL IS OUR PROPERTY AND MUST NOT BE USED EXCEPT IN CONNECTION WITH OUR WORK ALL RIGHTS OF DESIGN AND INVENTION ARE RESERVED THIS IS AN ELECTRONICALLY GENERATED DOCUMENT - DO NOT SCALE THIS PRINT						RFP	CAD FILE ee7308ad		SIZE	DRAWING NO. PAGE OF REV.
						DIST WA-LB			A	EE7308AD 8